

TECHNICAL DATA
DATA SHEET 5552, Rev. -

**SINGLE PHASE FULL WAVE
BRIDGE RECTIFIER ASSEMBLY**

DESCRIPTION: A 6 KV, 150 mA SINGLE PHASE RECTIFIER BRIDGE

FEATURES:

- Low thermal resistance
- Add suffix "S" for S-100 screening
- Compact low stress package

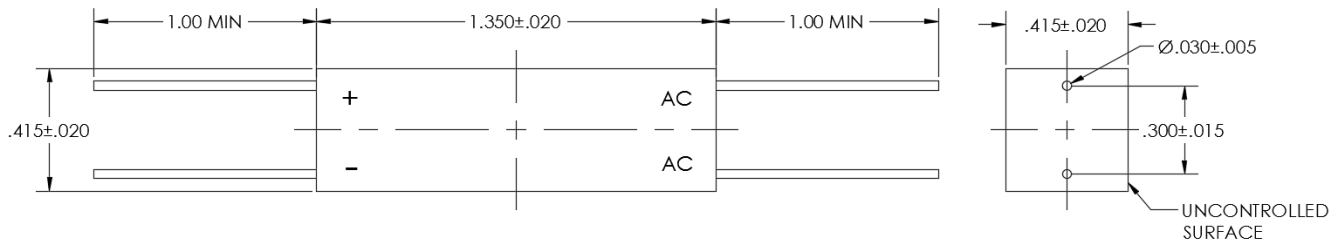
ELECTRICAL CHARACTERISTICS

All ratings are at $T_A = 25^\circ\text{C}$ and characterized per leg unless otherwise specified.

CHARACTERISTIC	SYMBOL	Min	MAX.	UNITS
WORKING PEAK INVERSE VOLTAGE	PIV		6	kV
MAXIMUM AVERAGE DC OUTPUT CURRENT	I_o		150	mA
MAXIMUM FORWARD VOLTAGE DROP ($I_f = 100$ mA per leg)	V_f		9.9	Volts
MAXIMUM REVERSE CURRENT at PIV (Per Leg) $T_J = 25^\circ\text{C}$ $T_J = 100^\circ\text{C}$	I_{R1} I_{R2}		5.0 100.0	μA
MAXIMUM REVERSE CURRENT (6 KV across both AC Input Terminals)	I_{R3}		10.0	μA
REVERSE RECOVERY TIME $I_f = 10\text{mA}$, $I_R = 10\text{mA}$, $I_{RR} = 5\text{mA}$ (Measured on discrete rectifiers prior to assembly)	t_{rr}		1.5	μsec
MAXIMUM SINGLE CYCLE SURGE CURRENT ($t_{p1} = 8.3$ ms, pulse)	I_{FSM1}		6	A
MAXIMUM SINGLE CYCLE SURGE CURRENT (across "+" and "-" terminals, $t_{p2} = 20$ μs , pulse)	I_{FSM2}		1200	A
MAXIMUM SHORT TIME OVERLOAD CURRENT ($t_{p3} = 5$ s, pulse)			1.25	A
MAXIMUM THERMAL RESISTANCE PER LEG (Junction to Case)	$R_{\theta JC}$		12	$^\circ\text{C/W}$
JUNCTION AND STORAGE TEMPERATURE	T_J & T_{STG}	-55	150	$^\circ\text{C}$

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MECHANICAL DRAWING



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